

Electronic Information Disclosure Statement

ENCAPSULATION OF ELECTRONIC DEVICES WITH SHAPED SPACERS

JC474 U.S. PTO
10/065254
09/30/02

Application:

Confirmation:

Applicant(s): Ewald Guenther

Docket Number: 2002P12562US

Group Art






Unit:





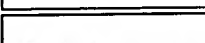

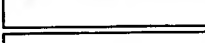

Examiner:

search string: (5550408 or 5724230 or 5777386 or 5844315 or 5900675 or 5939783 or 6172879 or 6177729 or 6232667 or 6260264 or 6313528 or 6339254 or 6376917).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
	P01	5550408	1996-08-27		Kunitomo et al	257	737
	P02	5724230	1998-03-03		Poetzinger	361	758
	P03	5777386	1998-07-07		Higashi et al	257	737
	P04	5844315	1998-12-01		Melton et al	257	738
	P05	5900675	1999-05-04		Appelt et al	257	778

P06	5939783	1999-08-17		Laine et al	257	702
P07	6172879	2001-01-09		Cilia et al	361	778
P08	6177729	2001-01-23		Benenati et al	257	738
P09	6232667	2001-05-15		Hultmark et al	257	777
P10	6260264	2001-07-17		Chen et al	29	832
P11	6313528	2001-11-06		Solberg	257	686
P12	6339254	2002-01-15		Venkateshwaran et al	257	686
P13	6376917	2002-04-23		Takeshita et al	257	778

Remarks

(Remarks are not for responding to an office action.)

Hayes et al, "Low-cost Display Assembly and Interconnect using Ink-jet Printing Technology", Journal of the SID 9/1, 2001, Pg 9-13

Signature

Examiner Name	Date